

LSI Logic

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LSI Logic Corporation  
Legal Department – IP  
1621 Barber Lane, MS D-106  
Milpitas, CA 95035Date October 20, 2003Number of pages including cover sheet 2

To: US PTO  
Mail Stop Status Request  
TC 2812  
Fax No. 703-872-9318  
Phone No.  
CC:

From: Connie del Castillo  
Intellectual Property Paralegal  
Telephone No. (408) 433-7191  
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## REMARKS:

Urgent     For your review     Reply ASAP     Please comment

Application Number: 09/731,596  
Filing date: December 6, 2000  
First named inventor: Margaret Fyfield  
Attorney docket number: 100.365 / 00-103

Transmitted herewith for filing via facsimile:

- Status Request Letter

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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re Application of:**

Margaret Fyfield

**Serial No. :**

09/731,596

Filed:

December 06, 2000

For i

## Probing Fixture And Method For Semiconductor Wafer

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Group Art Unit : 2812

Examiner : Kik, P.

Atty Docket : 100.365 / 00-103

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## **STATUS REQUEST**

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicants request a status of the above referenced application. Please mail the status notification to the address stated below.

Respectfully submitted,

ZSKW

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Date: 20 Oct 03